

Fig. 1 Prior Art

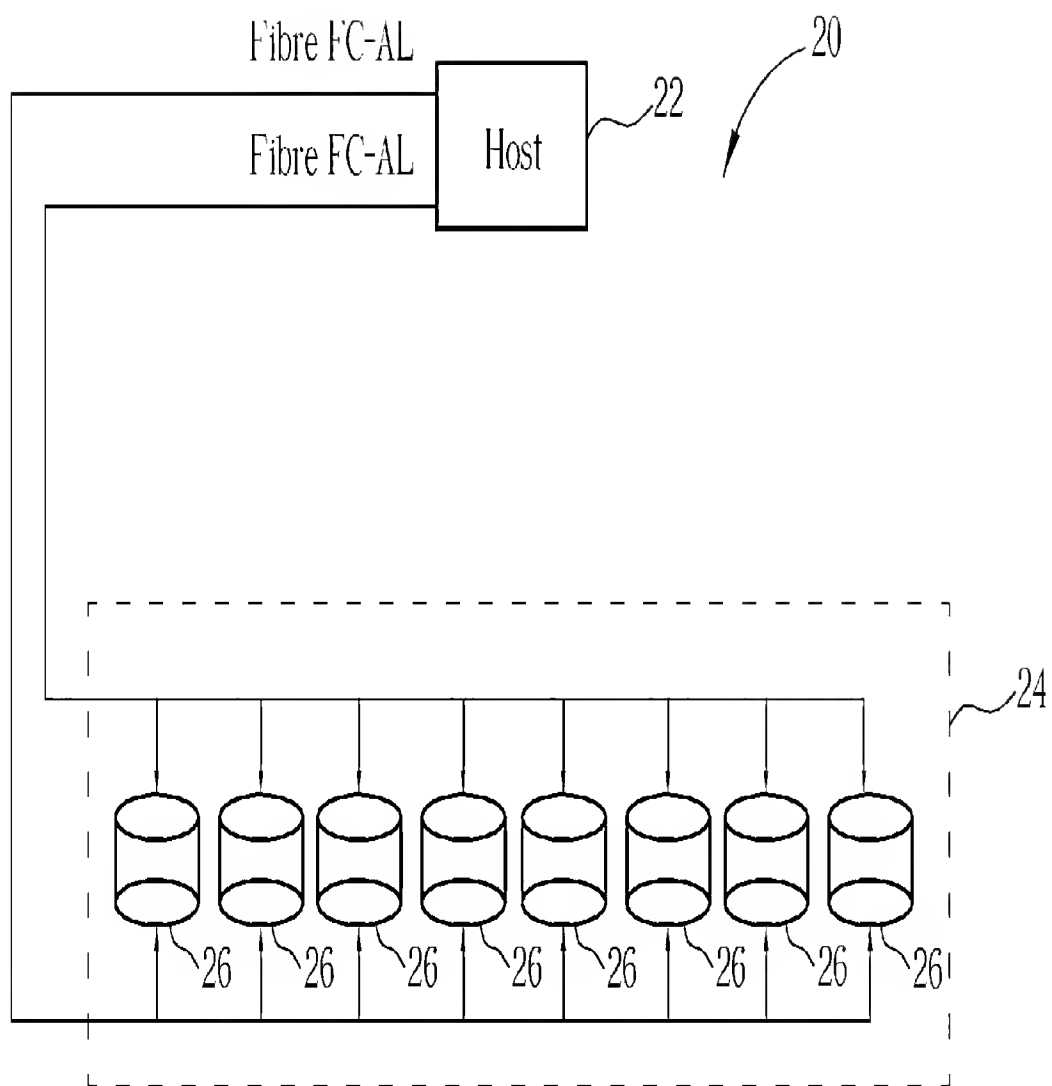


Fig. 2 Prior Art

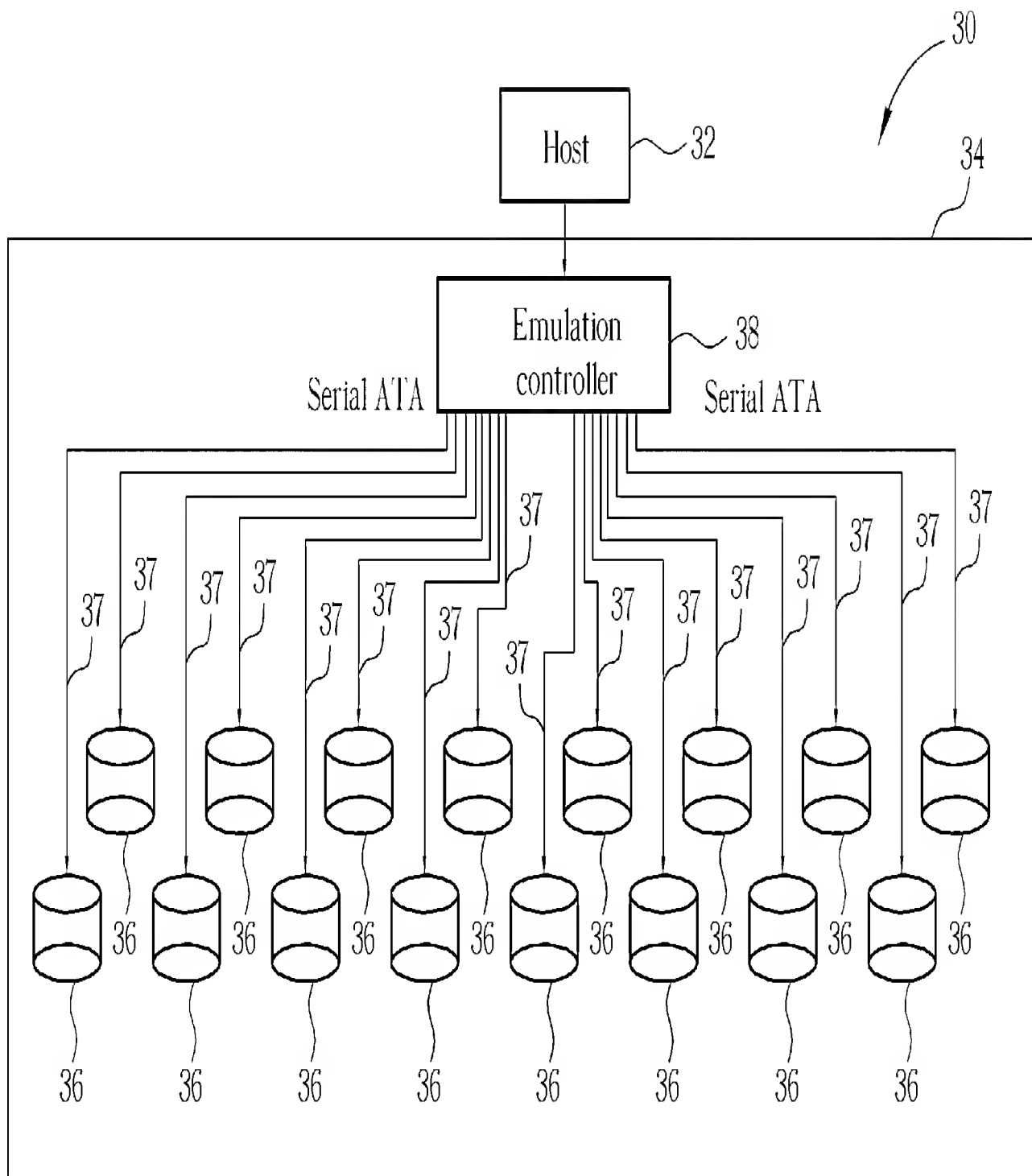


Fig. 3

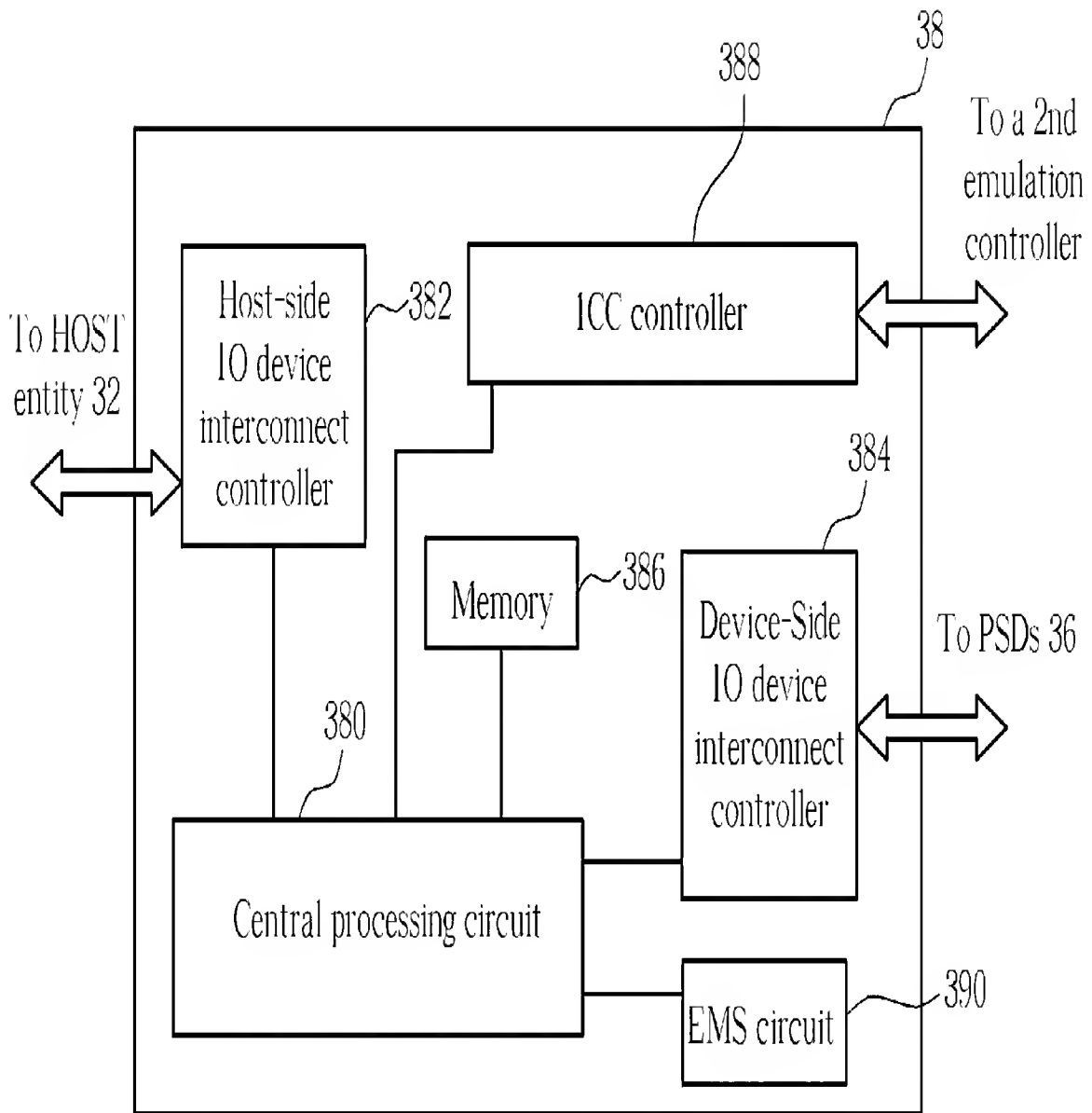


Fig. 4

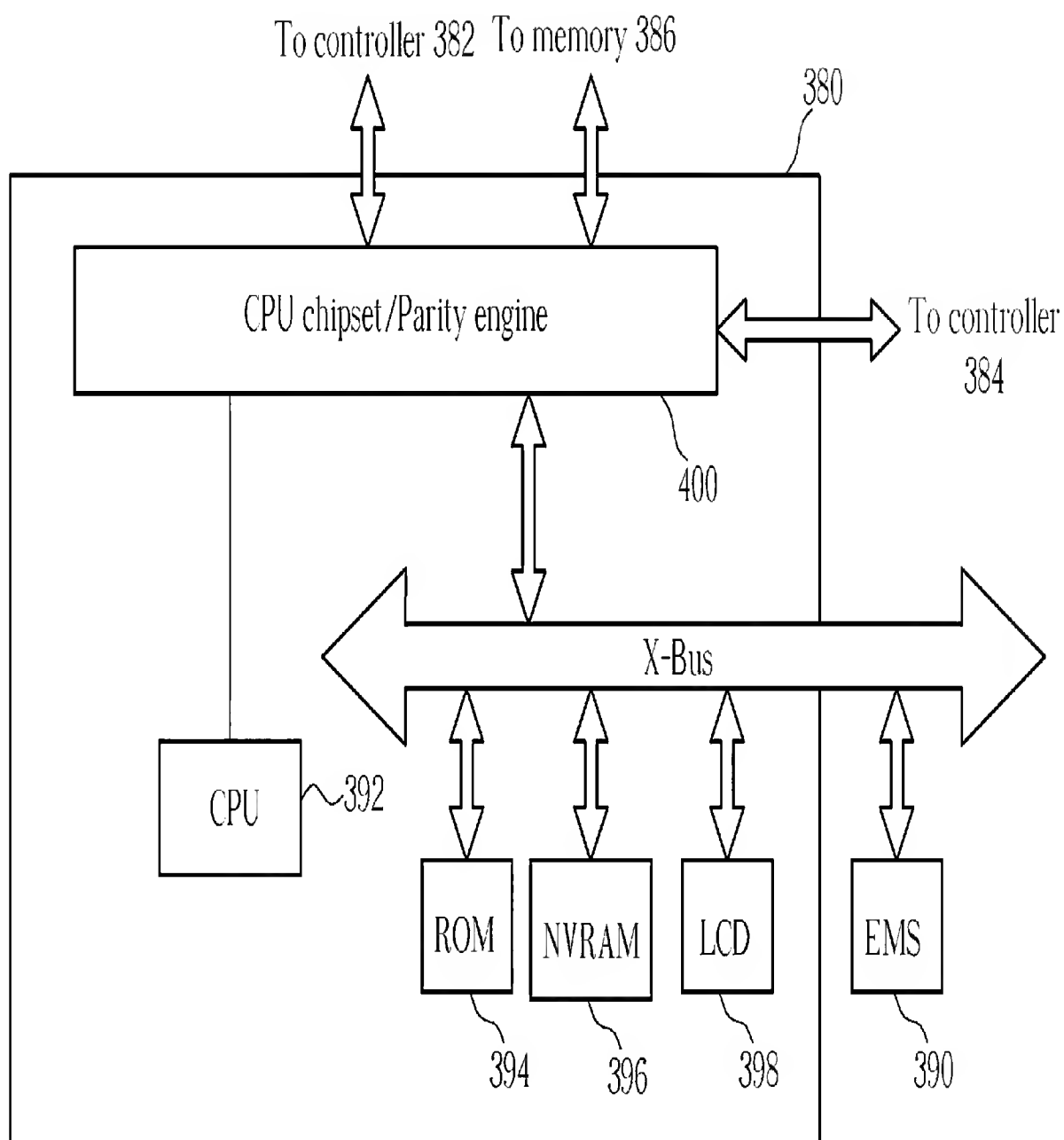


Fig. 5

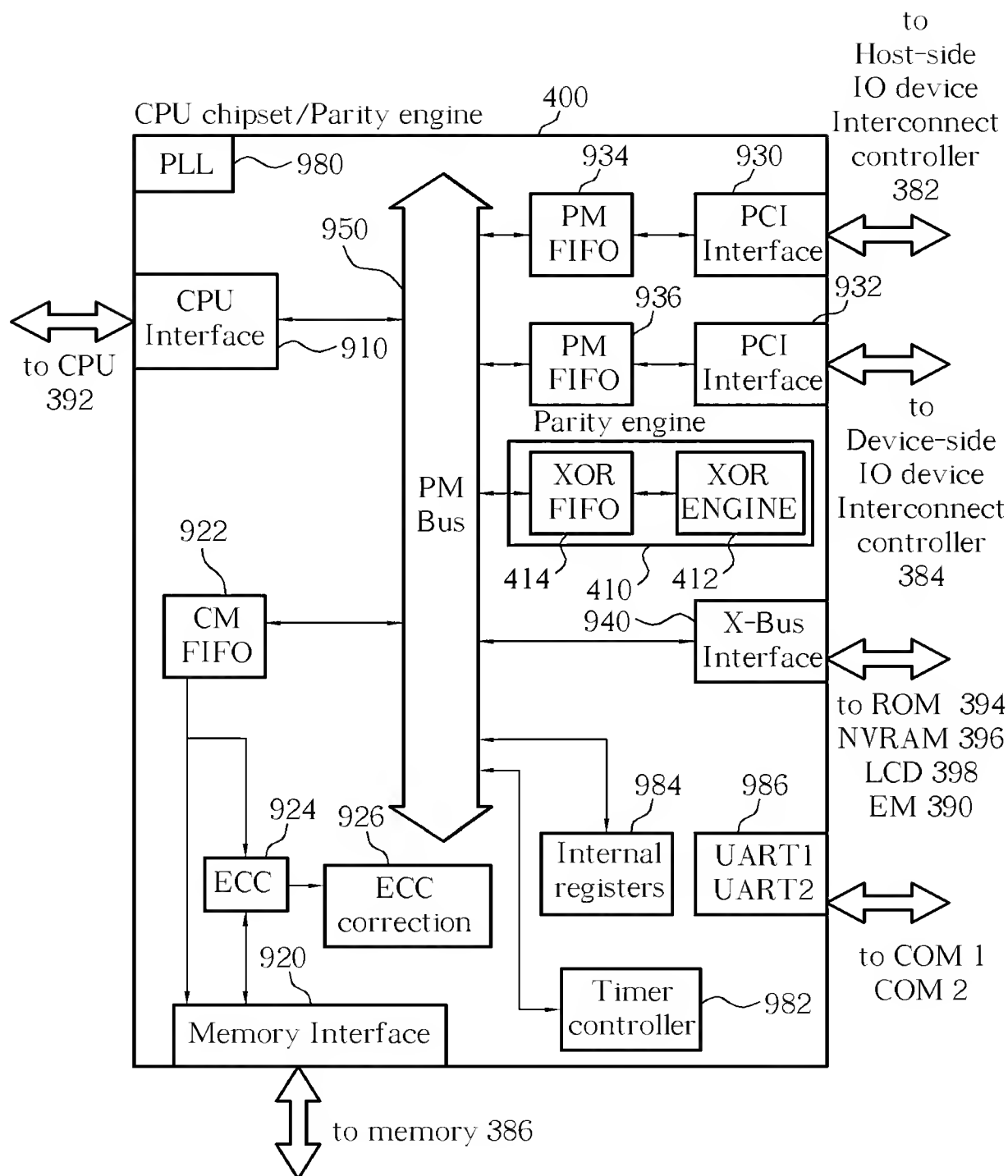


Fig. 6

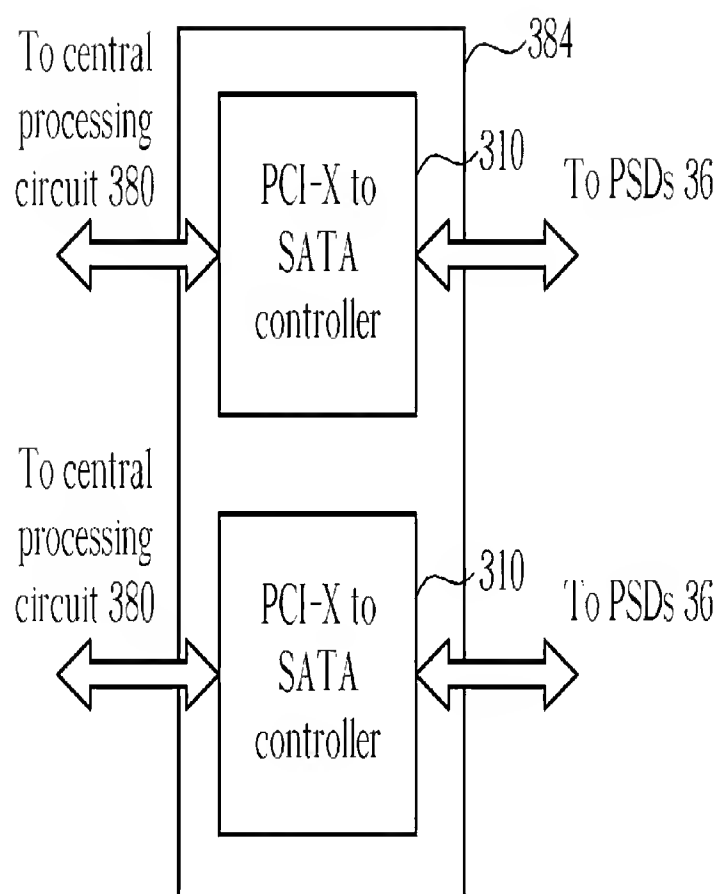


Fig. 7

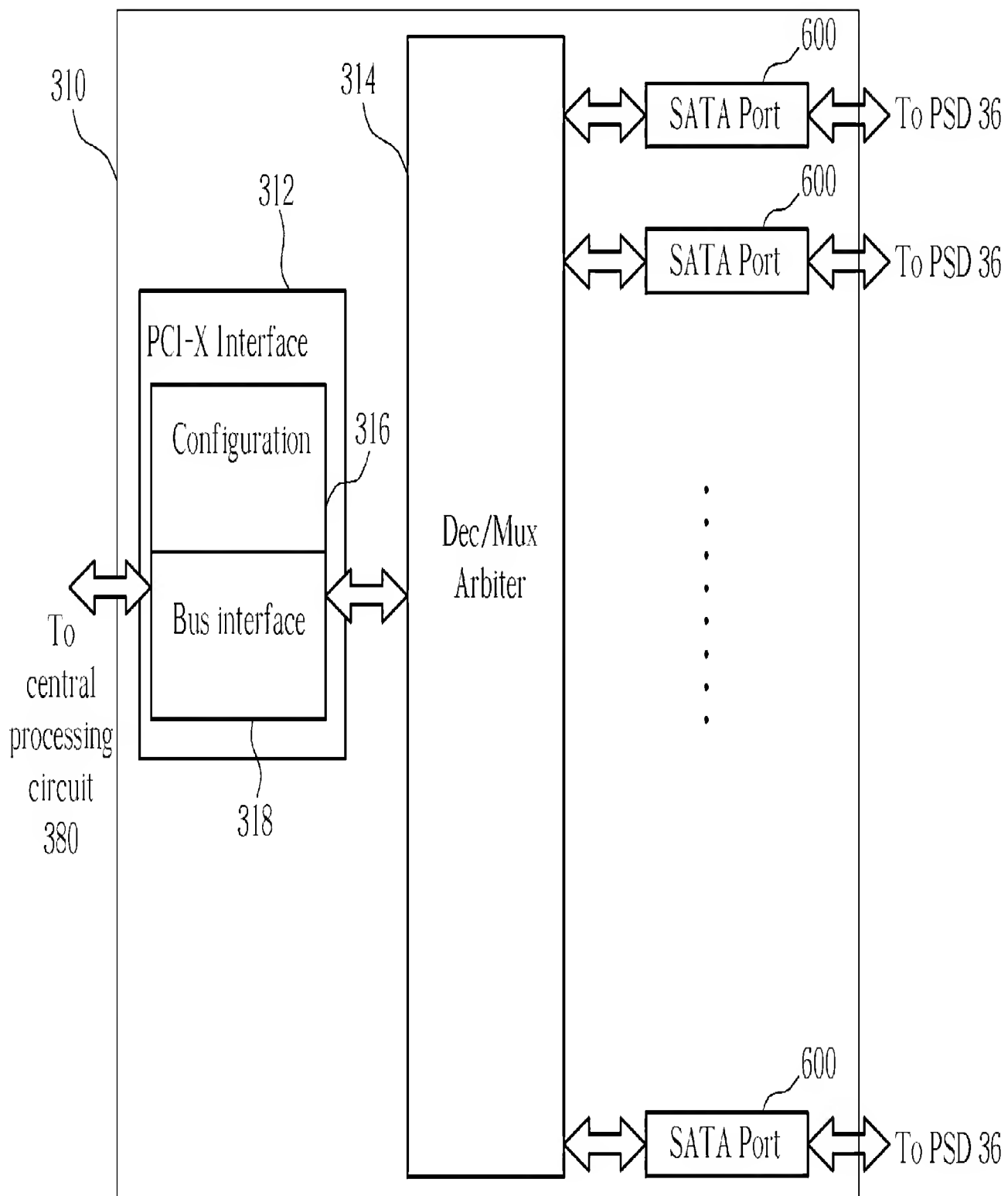


Fig. 8

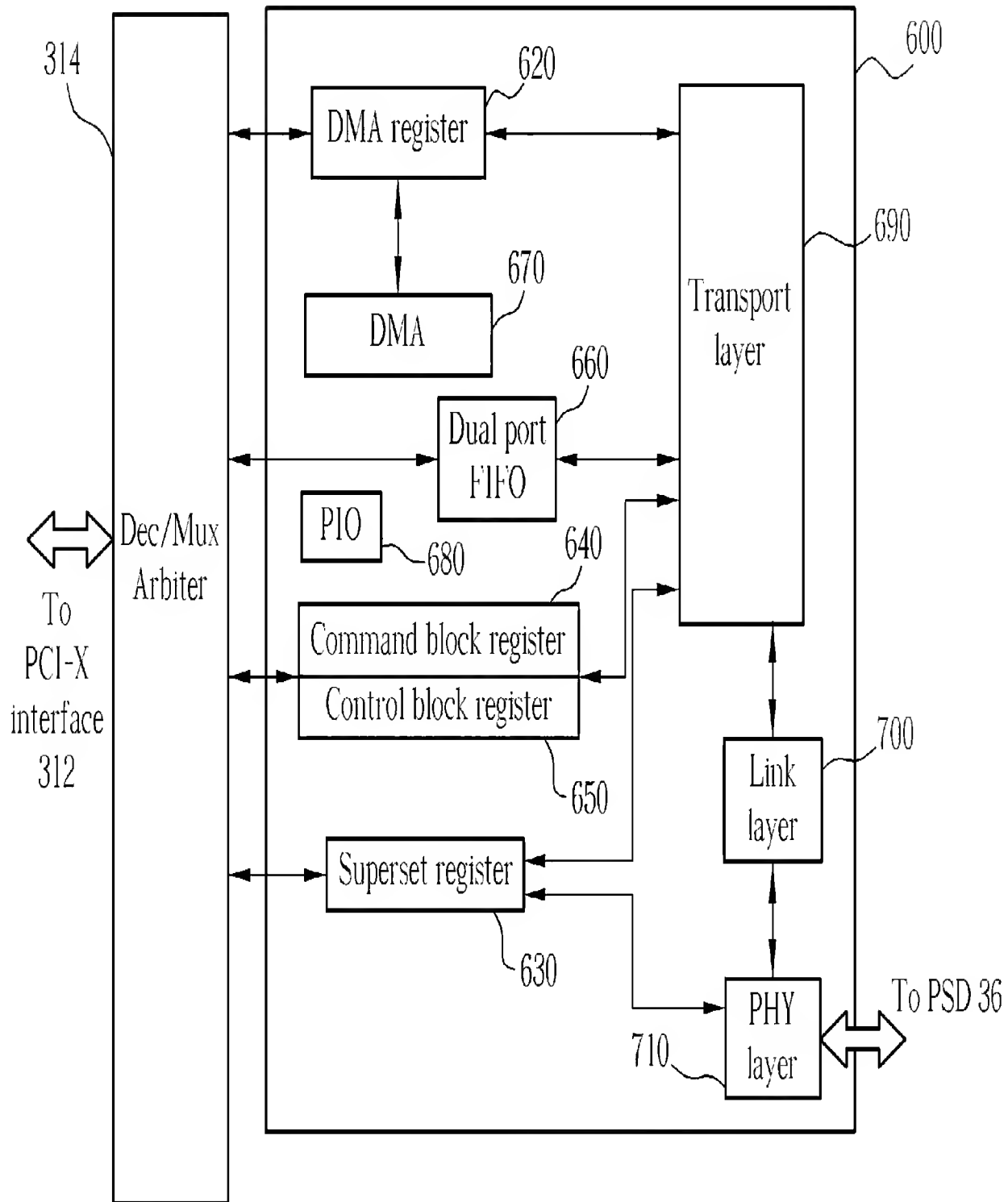


Fig. 9

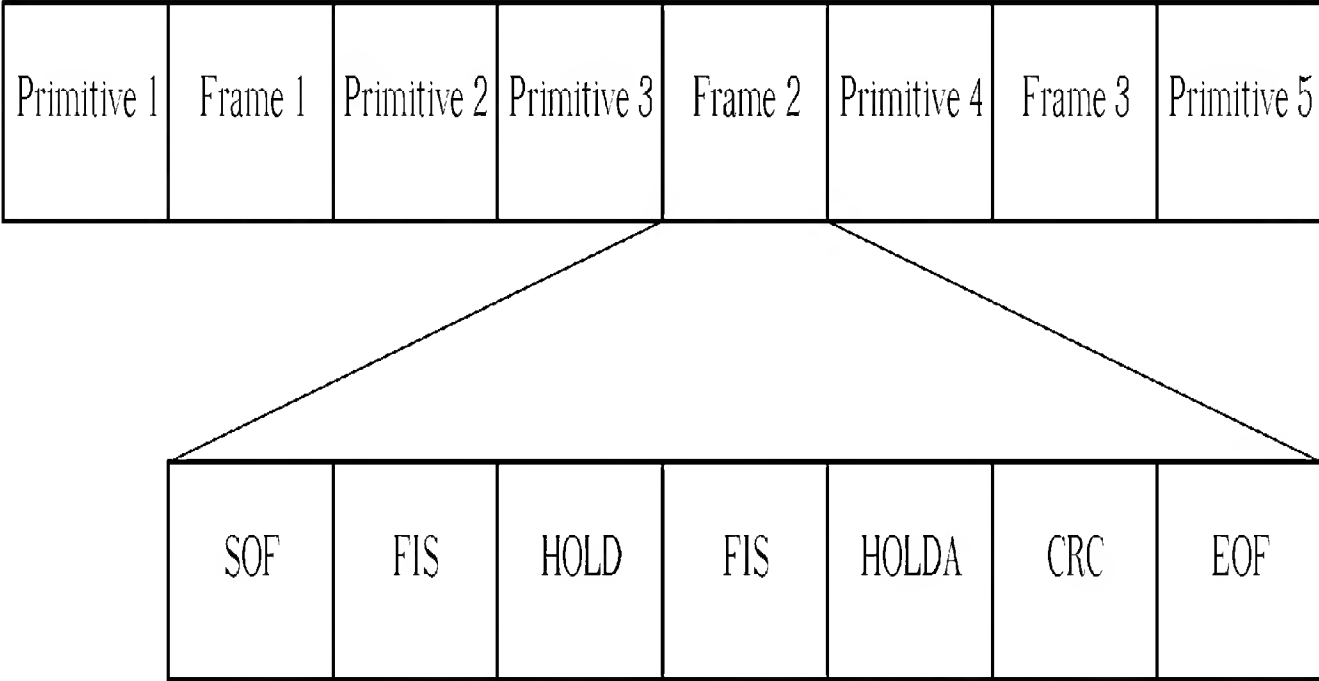


Fig. 10

0	Reserved (0)	Reserved (0)	R	I	D	Reserved (0)	FIS Type (41h)
1						DMA buffer identifier Low	
2						DMA buffer identifier High	
3						Reserved (0)	
4						DMA buffer offset	
5						DMA transfer count	
6						Reserved (0)	

Fig. 11

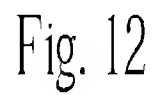


Fig. 12

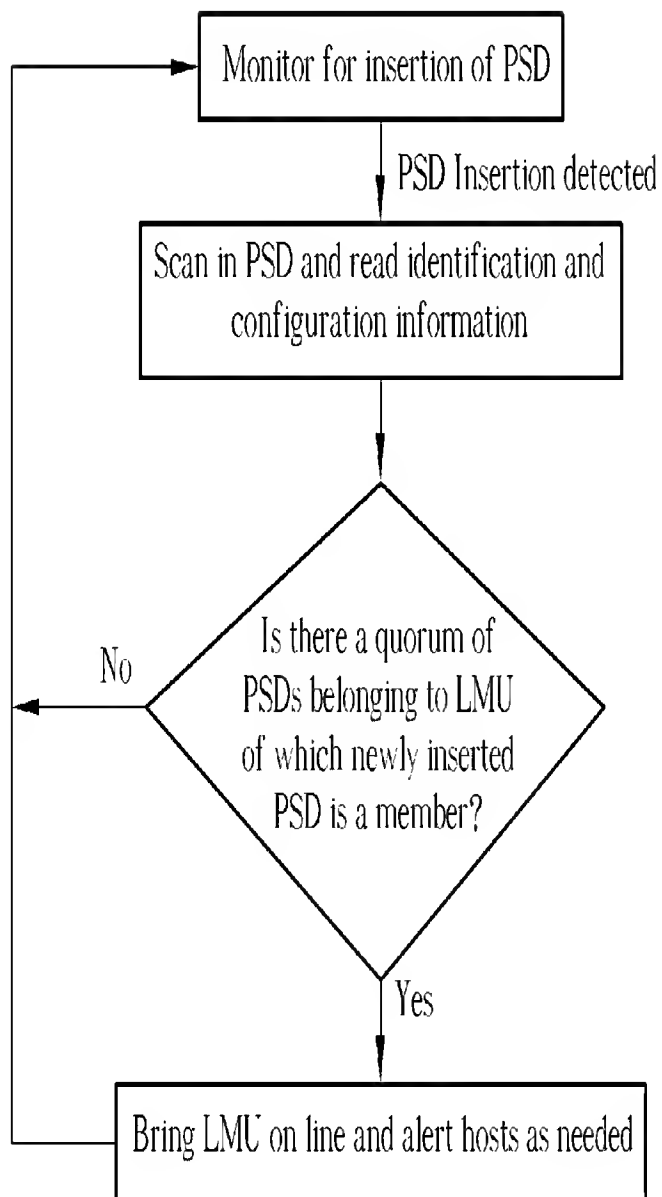


Fig. 13

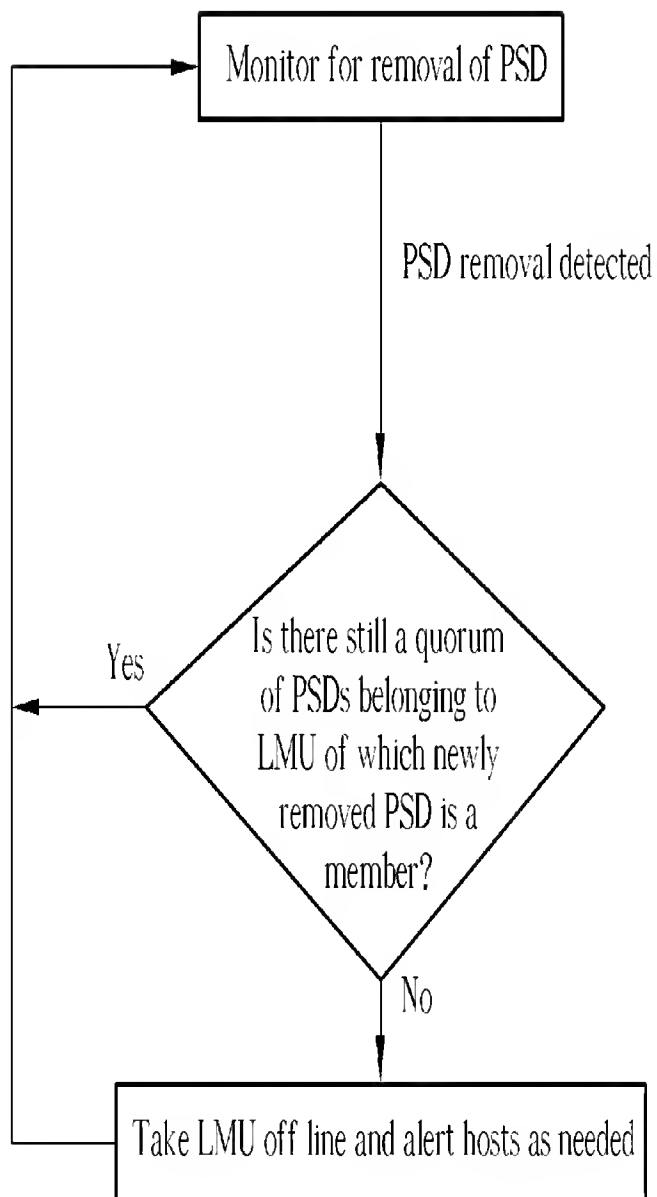


Fig. 14

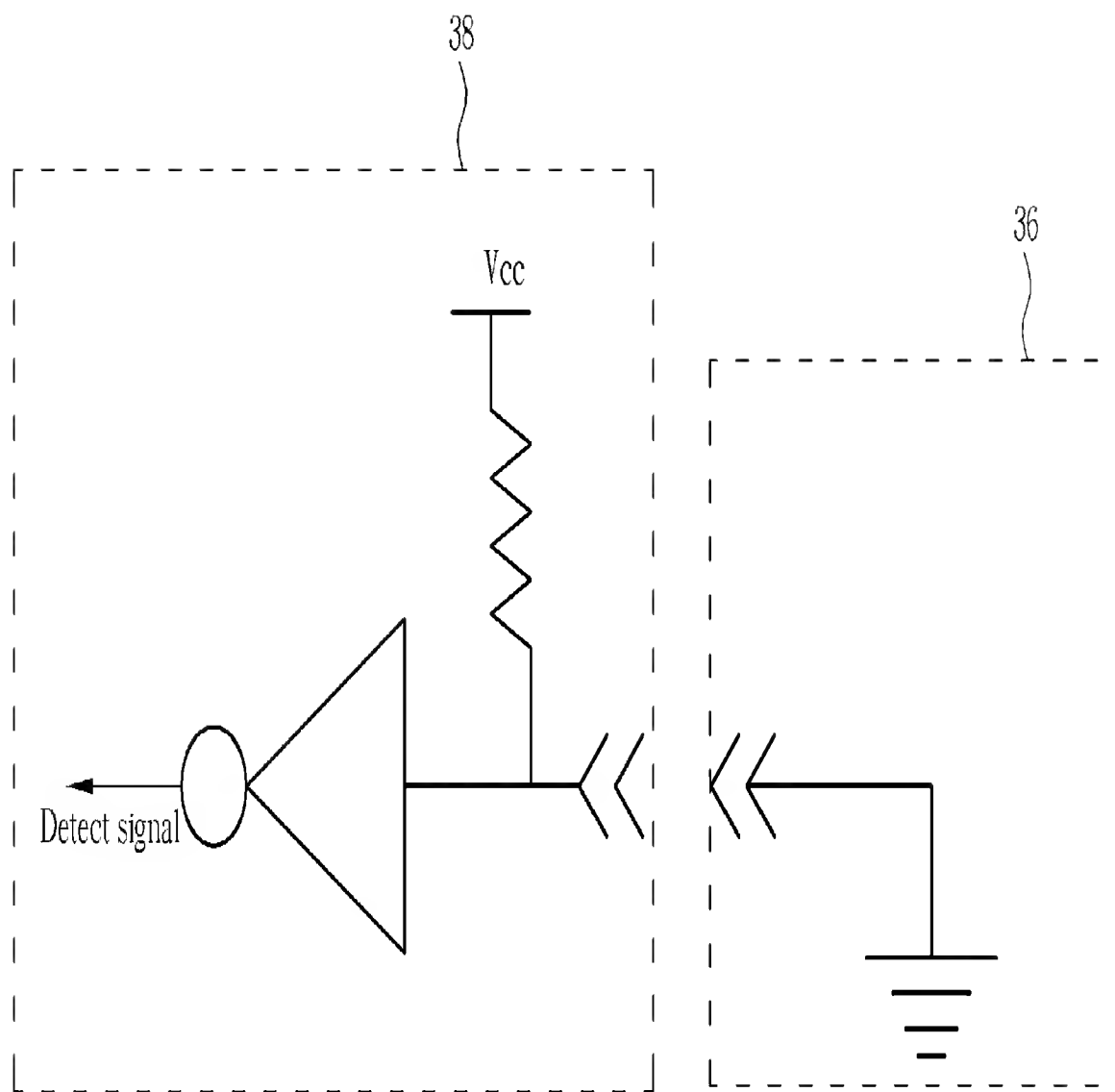


Fig. 15

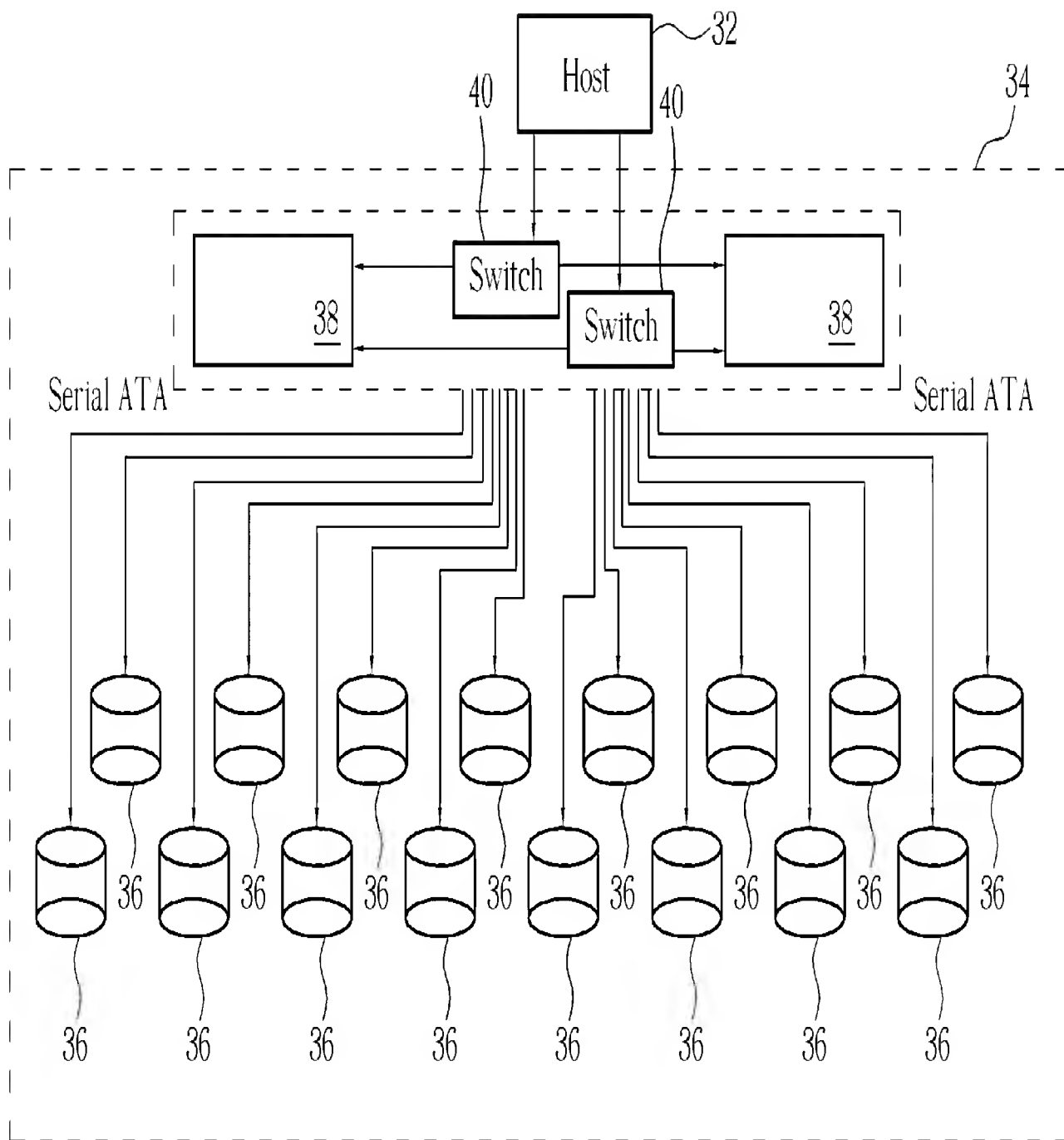


Fig. 16

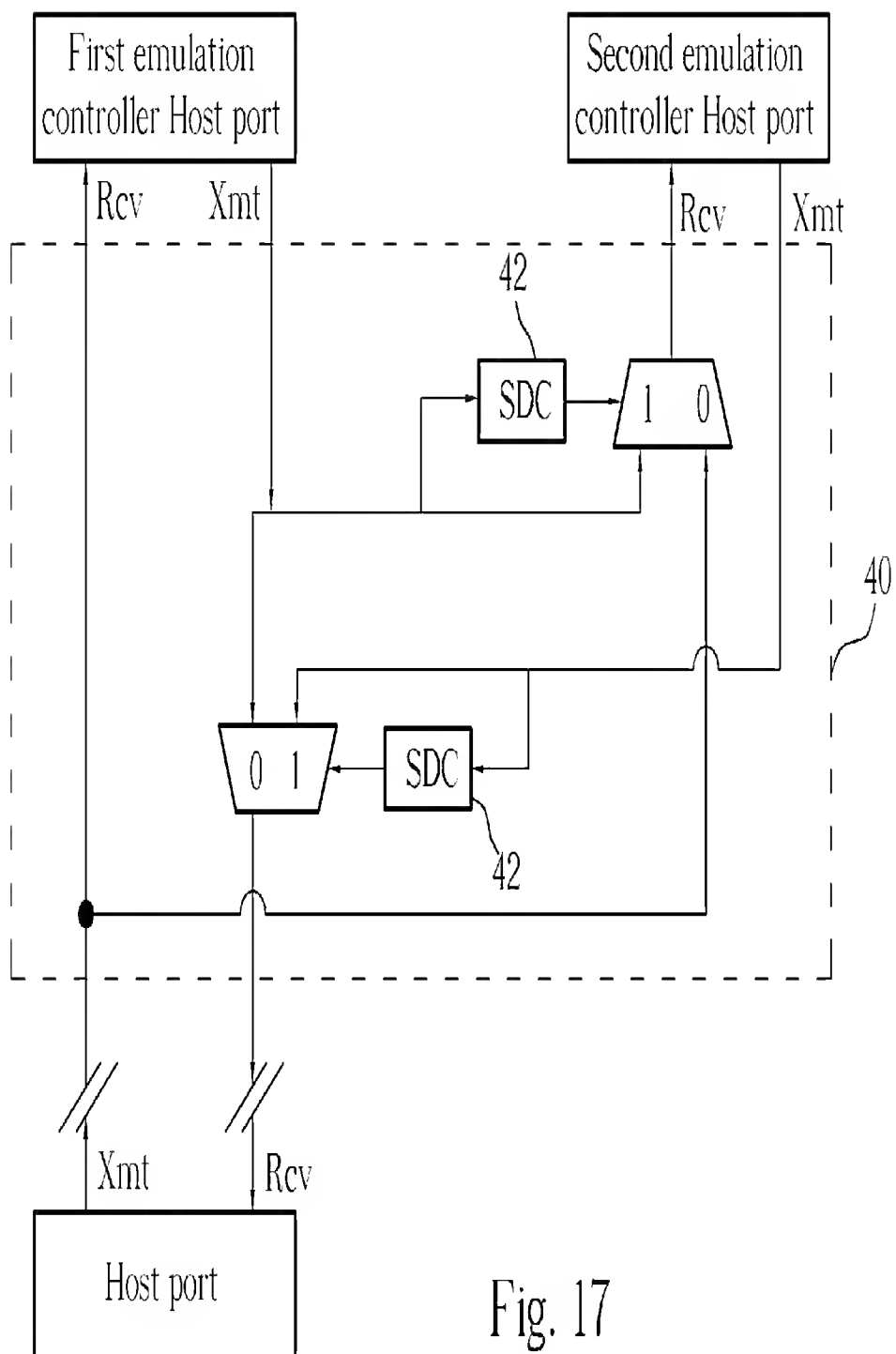


Fig. 17

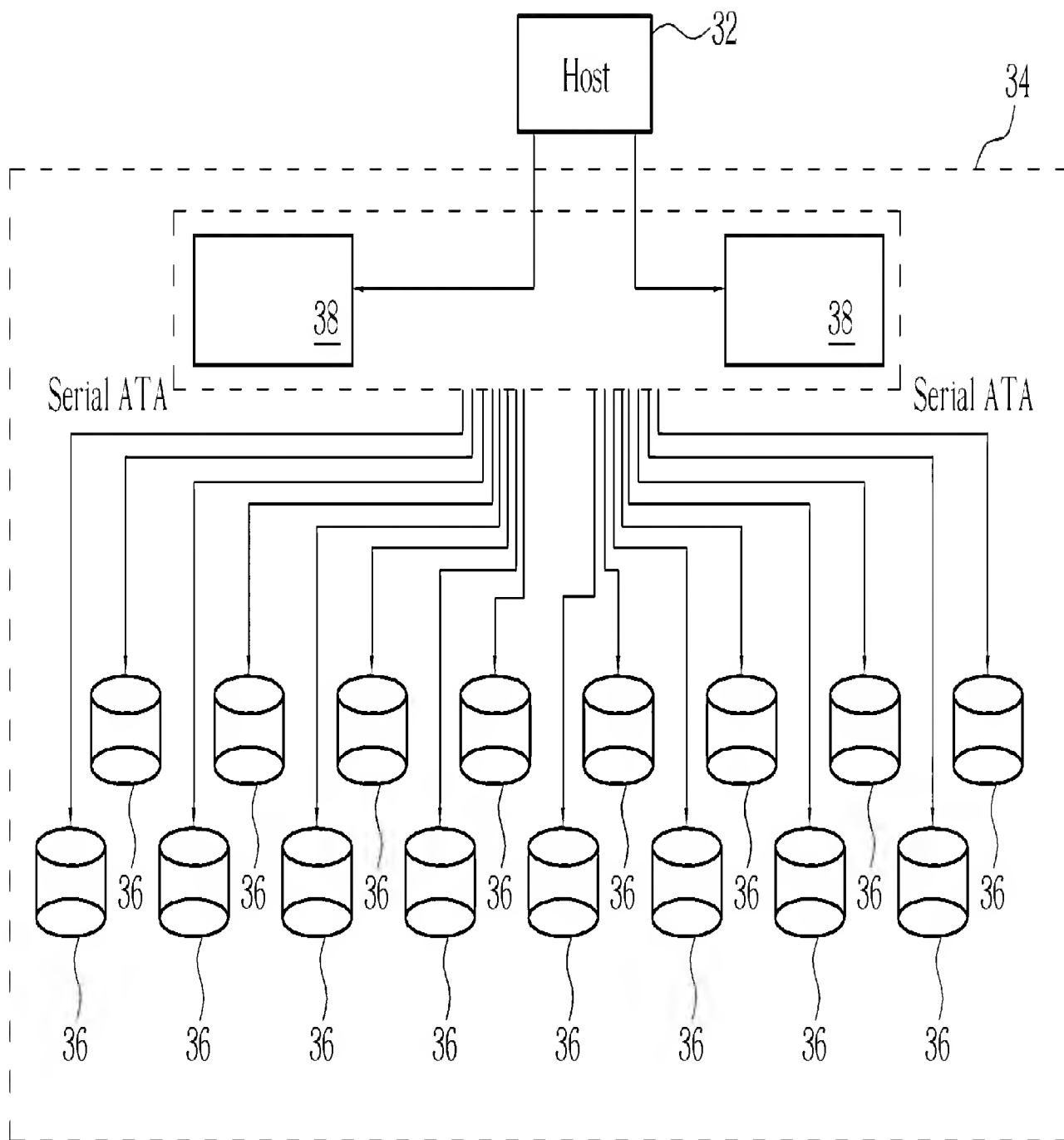


Fig. 18